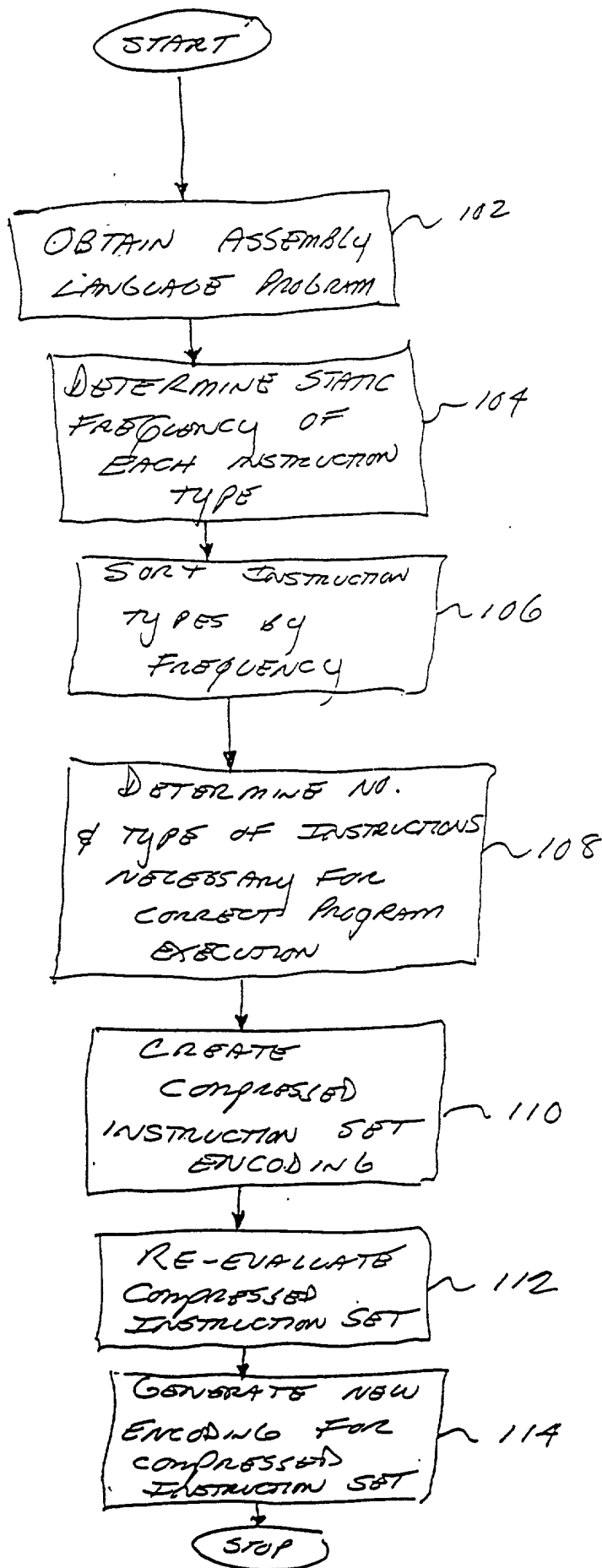


FIG. 1

FIG. 1

100



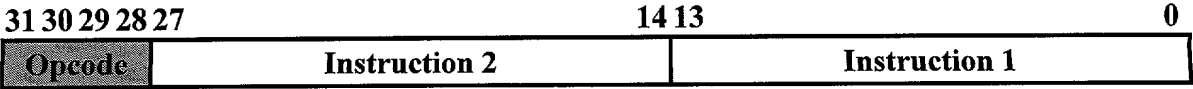


Fig. 2

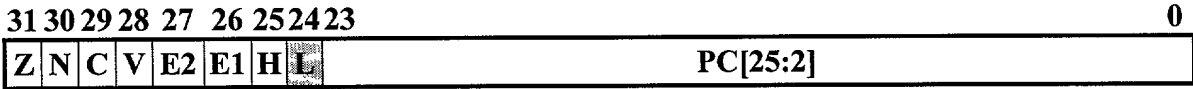


Fig. 3

13	12	11	10	9	8	7	6	5	4	3	2	1	0
0	0	0	cc			8 bit signed offset							

Fig. 4

13	12	11	10	9	8	7	6	5	4	3	2	1	0
0	0	1	a			b			op				

Fig. 5

13	12	11	10	9	8	7	6	5	4	3	2	1	0
0	0	1	a			subop			op=31				

Fig. 6

13	12	11	10	9	8	7	6	5	4	3	2	1	0
0	0	1	Imp_op			subop=7			op=31				

Fig. 7

13	12	11	10	9	8	7	6	5	4	3	2	1	0
0	1	0	A			b			op	4 bit offset			

Fig. 8

3	12	11	10	9	8	7	6	5	4	3	2	1	0
0	1	1	A			b			op	4 bit offset			

Fig. 9

13	12	11	10	9	8	7	6	5	4	3	2	1	0
1	0	0	A			op	7 bit offset						

Fig. 10

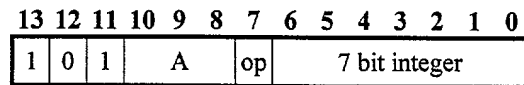


Fig. 11

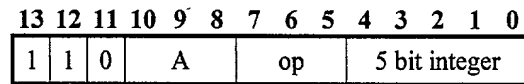


Fig. 12

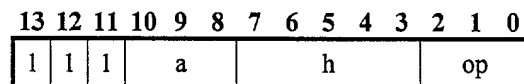


Fig. 13

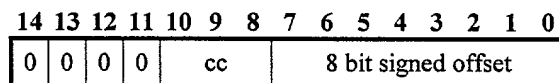


Fig. 14

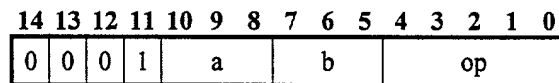


Fig. 15

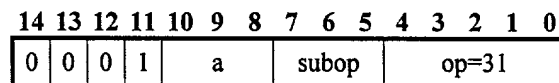


Fig. 16

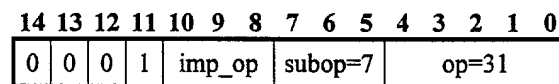


Fig. 17

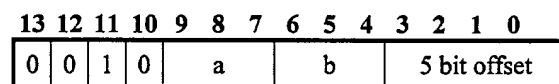


Fig. 18

13	12	11	10	9	8	7	6	5	4	3	2	1	0
0	0	1	1		a		b						5 bit offset

Fig. 19

14	13	12	11	10	9	8	7	6	5	4	3	2	1	0
0	1	0	0		a		op							7 bit offset

Fig. 20

14	13	12	11	10	9	8	7	6	5	4	3	2	1	0
0	1	0	1		a		op							7 bit integer

Fig. 21

14	13	12	11	10	9	8	7	6	5	4	3	2	1	0
0	1	1	0		a		op							5 bit integer

Fig. 22

14	13	12	11	10	9	8	7	6	5	4	3	2	1	0
0	1	1	1		a									op

Fig. 23

14	13	12	11	10	9	8	7	6	5	4	3	2	1	0
1	0	0	0		a		b		0	op				c

Fig. 24

14	13	12	11	10	9	8	7	6	5	4	3	2	1	0
1	0	0	0		a		b		1	op				3 bit int

Fig. 25

14	13	12	11	10	9	8	7	6	5	4	3	2	1	0
1	0	0	1		a		op							7 bit integer

Fig. 26

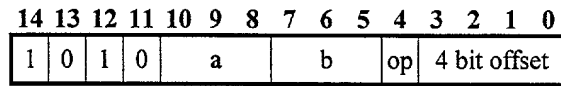


Fig. 27

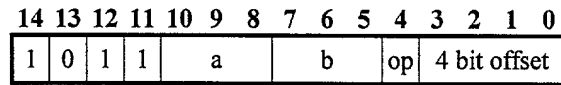


Fig. 28

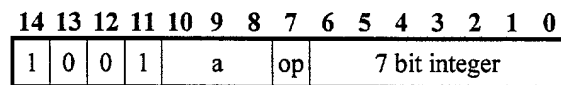


Fig. 29

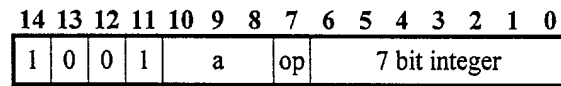


Fig. 30

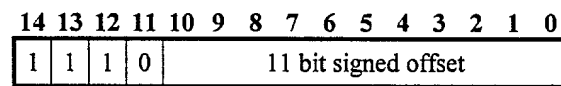


Fig. 31

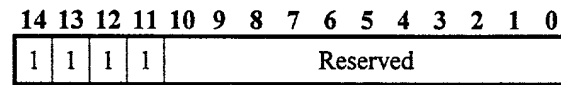
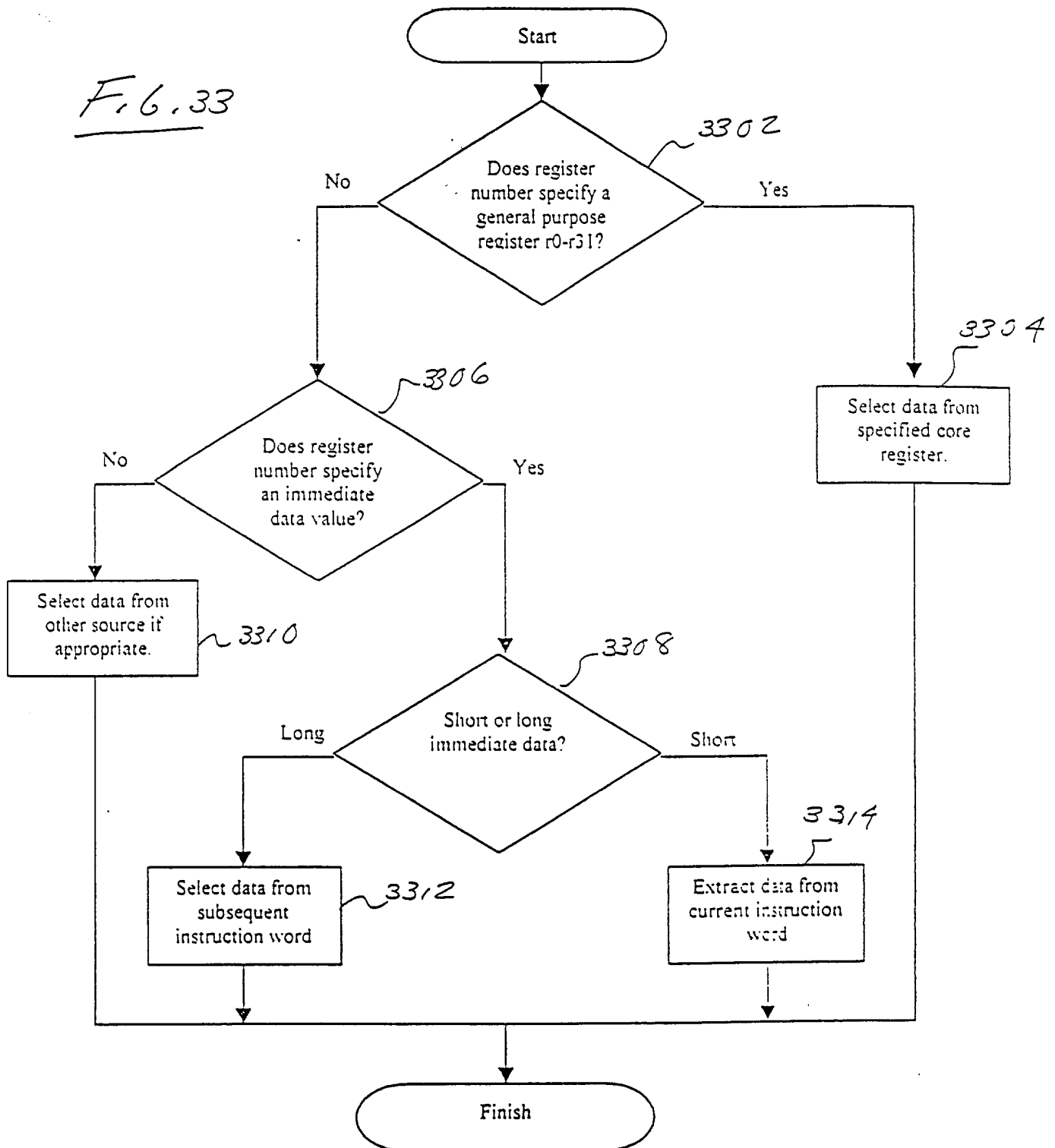


Fig. 32

F.6.33



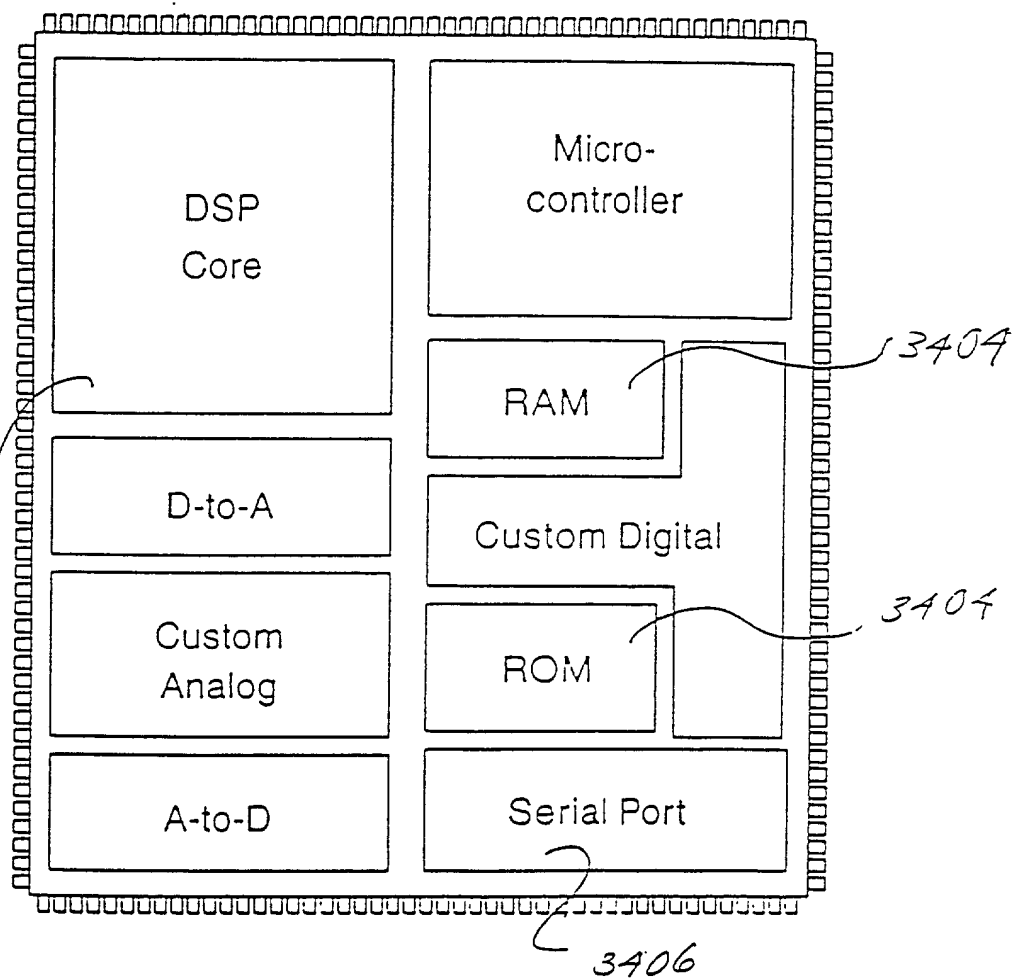


FIG. 34

FIG. 35

